

Date:	20.05.2014	Version (History):	1.0
Subject:	FW V3.6.16/20 for SPEED7 CPUs	Author:	Markus Dörnhöfer

History Firmware 3.6.15 for SPEED7 CPUs

Firmware V3.6.16/20 for SPEED7 CPUs 31x-xxxxx is available for download from www.vipa.com/de/service-support/downloads/firmware/ with immediate effect. The firmware is compatible to its previous versions.

Additional information and downloads are to find at www.vipa.com

Software changes for CPUs

- It's now possible to activate/deactivate Slave's by using SFC12.
- Correct online View for I-Device.
- Buffer-over-flow of TCP-Communication solved.
- SF-LED functionality for EtherCAT CPU optimized.
- Increasing of the Local-Data to 3096 for Prio 1 and 2048 for Prio 2 for CPU's which are configured as 315/317 from Siemens.
- Open UDP-Communication has been expanded.
- Timestamps for OB20 have been changed.
- Changing the I-Slave-address using SFC75 now possible.
- DP-V1 Dataset read/write also possible to Slaves with error possible.
- Alarm handling for OB86 and OB82 for EtherCAT Master expanded.
- SZL list for EtherCAT CPU extended (0xXY91, 0xXY94, 0xXY96).
- No CPU failure through illegal access using TCP (e.g. Scan with "Nessus" Software).
- Force functionalities extended, force-values are remnant now.

Hardware changes for CPUs

CPU's 31x-xXXx2:	AS-Stand old: AS2	new: AS3
CPU's 31x-xXXx3 (configurable via TIA-Portal):	AS-Stand old: AS1	new: AS2

- Using of new internal memory pin-and-socket connectors
- Improved MPI interface protection against overvoltage
- Unlimited PROFIBUS communication for additional bus-subscribers during PowerFail and PowerUp of the CPU
- Improved real-time-clock-accuracy when using the Ethernet PG/OP-Interface
- Over-tension protection for drawing 24V over the MPI-Interface (e.g. OP03)
- Improved power supply: no shut down with short 24V-instabilities
- Usage of several new electronic elements because of component-proclamation from component manufacturers